

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Satoshi Ishikura

Title: SEMICONDUCTOR DEVICE, SEMICONDUCTOR DEVICE DESIGN
METHOD, SEMICONDUCTOR DEVICE DESIGN METHOD
RECORDING MEDIUM AND SEMICONDUCTOR DEVICE DESIGN
SUPPORT SYSTEM

Docket: 31638US4

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. 1.98(d), copies of the references are not enclosed because they were previously cited by or submitted to the Patent Office in U.S. Patent Application Serial No.10/278,496, which application is relied upon for an earlier filing date under 35 U.S.C. 120.

If there are any fees resulting from this communication, please charge said fees to our Deposit Account No. 16-0820, Order No. 31638US4.

Respectfully submitted,

PEARNE & GORDON LLP

By: _____

Michael W. Garvey, Reg. No. 35878

526 Superior Avenue East
Suite 1200
Cleveland, Ohio 44114-1484
(216) 579-1700

Date: June 20, 2003

Form PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 31638US4	SERIAL NO. N/A
INFORMATION DISCLOSURE CITATION BY APPLICANT (USE SEVERAL SHEETS IF NECESSARY)		APPLICANT: Satoshi Ishikura	
		FILING DATE: herewith	GROUP ART UNIT:

U.S. PATENT DOCUMENTS

Examiner Initial		Document No.	Date	Name	Class	Subclass	Filing Date If Appropriate
	A	4,656,592	4/1987	Spaenenburg et al.			
	B	5,500,542	3/1996	Iida et al.			
	C	5,672,895	9/1997	Iida et al.			
	D	5,736,772	4/1998	Ko et al.			
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	F	5,854,752	12/1998	Ararwal			
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	L	6-61440 A	3/1994	JP			
	M	6-204467	7/1994	JP			
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	R	8-339392	12/1996	JP			

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

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Examiner:	Date Considered
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***Examiner:** Initial if reference considered, regardless of whether citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.